

Application No. 10800938 (Docket: CNTR.2072)
37 CFR 1.111 Amendment dated 10/24/2007
Reply to Office Action of 08/20/2007

AMENDMENTS TO THE ABSTRACT

Please delete the section entitled "ABSTRACT OF THE DISCLOSURE" in its entirety and substitute the following section therefor:

ABSTRACT OF THE DISCLOSURE

~~The present invention provides an apparatus and method for performing cryptographic operations on a plurality of input data blocks within a processor. In one embodiment, an apparatus~~
An apparatus for performing cryptographic operations ~~is provided.~~ The apparatus includes a ~~cryptographic instruction~~ fetch logic, algorithm logic, and execution logic. ~~The fetch logic is disposed within a microprocessor and is configured to receive a cryptographic instruction is received by a computing device as part of an instruction flow executing on the microprocessor.~~ The cryptographic instruction prescribes one of the cryptographic operations and one of a plurality of cryptographic algorithms. The algorithm logic is ~~disposed within the microprocessor and is operatively coupled to the cryptographic instruction and instruction.~~ The algorithm logic directs the ~~computing device~~ microprocessor to execute the one of the cryptographic operations according to the one of a plurality of cryptographic algorithms. The execution logic is ~~disposed within the microprocessor and operatively coupled to the algorithm logic and logic.~~ The execution logic executes the one of the cryptographic operations.